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China AI hardware

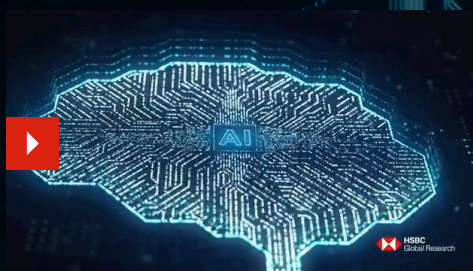
A manual for eight key sub-sectors

AI is driving up demand for more advanced computing power

We profile eight hardware sub-sectors that stand to benefit most

... and the opportunities and challenges from localisation

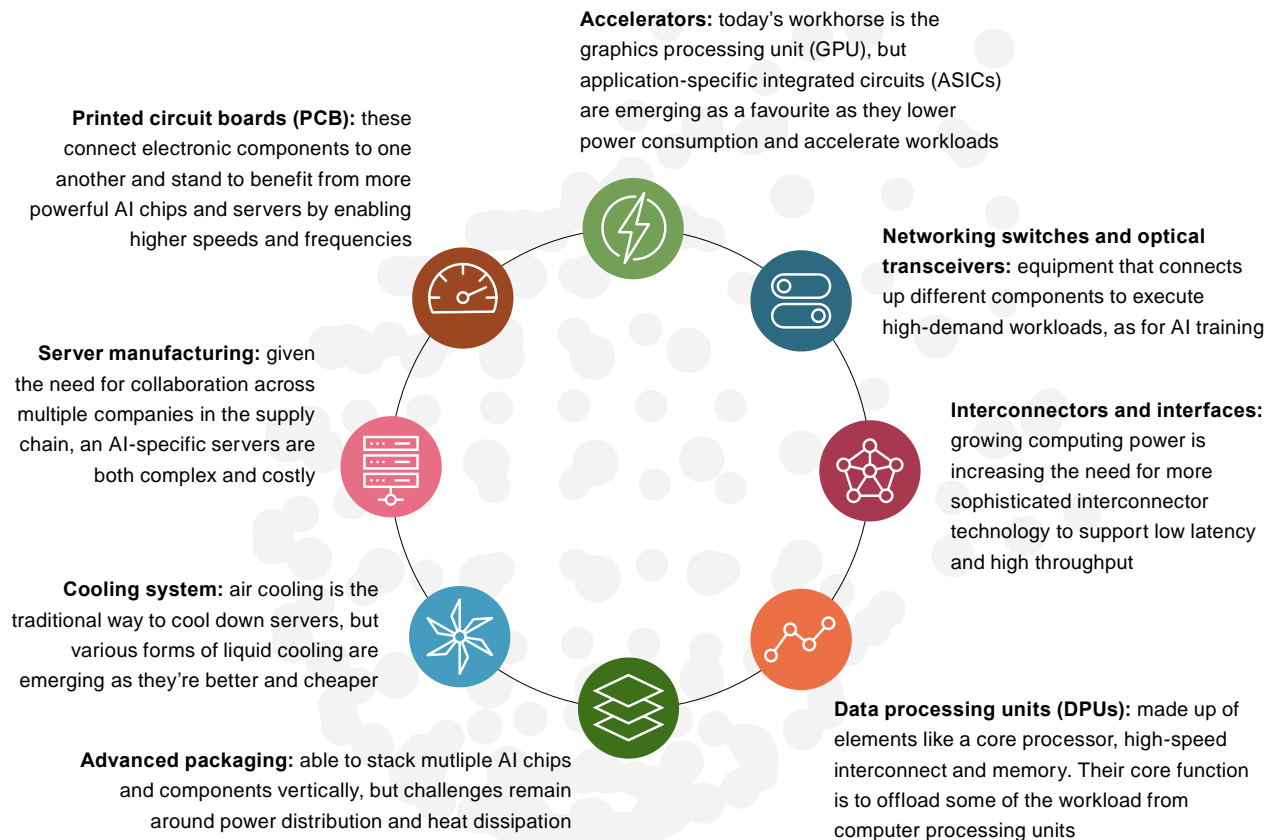
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Sector snapshot: China AI hardware

Which hardware sectors are best positioned in the “Generative AI” ecosystem?



USD98bn

The PCB market is expected to grow from USD82bn in 2022 to USD98bn 2027e

49% CAGR

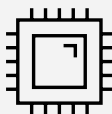
The global DPU market is set to increase from USD7.5bn in 2022 to USD24.5bn in 2025e

USD25.8bn

The advanced packaging market is expected to grow from USD9.2bn in 2022 to USD25.8bn in 2028e

Opportunities and challenges from localisation

Hardware



Challenges

US restrictions on China's procurement of advanced GPUs and accelerators from overseas suppliers

Opportunities

We see opportunities in AI hardware systems, such as chip architecture, processing nodes, interconnect bandwidth and memory capacity

Software



There remains a large gap between China and the global leaders when it comes to the development of large language models (LLMs)

More than 79 LLMs are now under development in China

China AI hardware

- ◆ AI is driving up demand for more advanced computing power ...
- ◆ ... so we map out the eight hardware sub-sectors that stand to benefit most
- ◆ We also detail opportunities and challenges from localisation

We see opportunities across the AI hardware supply chain

As generative AI applications – like OpenAI's ChatGPT and Google's Bard chatbots – grow bigger and better, we expect investment in the hardware that runs these tools to head higher. Our upbeat view on demand for AI computing infrastructure rests on two catalysts: (1) major cloud and technology companies are racing to develop and train their own large language models; (2) early movers in AI, such as OpenAI, are rushing to support their growing user base by making smarter inferences and training more advanced models.

The focus of this note is to provide a manual to the eight key sub-sectors that we expect to benefit most from technology upgrades as the industry races to meet rising demand for generative AI. We detail each sector's development roadmap, growth profile, and related companies.

1. Accelerators. These are the chips that speed up the processing of information-intensive operations like AI. The most common type today is the graphics processing unit (GPU), an all-rounder that is fairly robust. But another type, application-specific integrated circuits (ASICs), are emerging as a favorite as they can lower power consumption and accelerate certain workloads. Meanwhile, different types of memory technology are also being rolled out such as near-memory computing (processing close to where data resides) and in-memory computing (running computer calculations entirely in the computer memory). We also look at the so-called software stack for developing AI models – such as programming frameworks, toolkits and compilers – which also plays a critical role in accelerating performance.

2. Networking and optical transceivers. A typical data centre consists of servers, networking equipment and storage. Networking is a vital part as it connects up different components to form computing clusters which execute high-demand workloads like AI training and inferencing. However, given AI workloads require ultra-high bandwidth, low latency and zero signal loss, we expect strong demand for high-speed 25.6T/51.2T switches and 800G optical transceivers which transmit signals. Linear-drive pluggable optics (LPO) are emerging as a new technology as these can reduce power consumption in switches and transceivers, have lower latency speeds, and are less costly. And we believe even more advanced co-packaging optics (CPO) may become more common in two to three years. Finally, Google's optical circuit switches are also an alternative approach as they cut the total cost of ownership.

3. Interconnect IP and interface IC. The growing amount of computing power needed for AI applications is increasing the need for more sophisticated interconnect technology to support low latency speeds and high throughput. In particular, interconnect intellectual property (IP) is needed for a wide range of chip connections. We think memory expansion and pooling which allows dynamic memory allocation and therefore increases memory usage efficiency – is the key trend, as evidenced by the success of Nvidia's NVLink and NVSwitch technology though others continue to push a different Compute Express Link (CXL) based interconnect platform.

4. Data Processing Units (DPUs). These are increasingly popular devices inside data centre servers as they combine networking technology with AI and computing power. Their principle function is to offload some of the workload (like networking, storage and security management) from CPUs, which then frees up CPUs to focus on other important tasks. DPUs consist of a core processor, acceleration engine, network interface, a high-speed interconnect and memory. The global DPU market is set to increase from USD7.5bn in 2022 to USD24.5bn in 2025, implying a 49% CAGR, according to CCID. While cloud service providers generally have their own in-house DPU products, we see opportunities for independent DPU vendors, mainly in the telecoms and enterprise market.

5. Advanced packaging. As AI chips integrate more and more transistors to improve their computing power, it has become essential to use more advanced packaging. One form of technology is 2.5 dimensional packaging with TSMC's technology the most common choice for most AI chip makers, including Nvidia. 3D packaging – stacking dies vertically, and also led by TSMC – offers more integration but there are still some technical challenges around power distribution and heat dissipation. While China-based semiconductor companies are unable to seize front-end opportunities (like designing and manufacturing GPU chips, an area currently dominated by Nvidia, AMD and TSMC) as they lag behind in manufacturing technology, we expect China-based outsourced semiconductor assembly and test players to win back-end packaging orders for Nvidia and AMD's AI chips.

6. Cooling systems. One unwanted byproduct of high-performing servers is an awful lot of heat. Unless cooled, server performance can suffer and worse, catch fire. Blowing cool air across or around a server is the traditional approach. However, this isn't enough for modern high-performance computing which generates a formidable amount of thermal energy, since air is not an effective heat transfer medium. And too many cooling fans causes a lot of noise. Hence, liquid cooling – which can involve wrapping a loop containing cold water around the hottest components like CPUs and GPUs to absorb their heat – is increasingly being used by cloud server providers as it is power and cost efficient. BlueOcean says air cooling a 10kW server rack costs USD7.02 per watt and liquid cooling costs USD6.98, with the costs falling when the consumption power of a server rack increases to 20kW and 40kW respectively. Liquid cooling is the best method to cool server racks consuming 50kW or higher power.

7. Servers. The server manufacturing industry is particularly complex given the need for collaboration across the supply chain. We split the production process into 12 steps and detail the leaders along the way. We also conducted a bill of materials analysis and find AI servers are 17-39x more expensive than general-purpose servers. We also see signs China is catching up in servers given ByteDance and Alibaba have placed massive GPU orders with Nvidia (source: Pandaily, 14 June 2023).

8. Printed circuit boards (PCB). This sub-sector is likely to benefit from growing demand for high-performance AI servers and upgraded switches as it enables devices to better share information and communicate with each other. In a typical AI server (such as Nvidia's popular DGX), we estimate the PCB content value be four to five times more than a regular server driven by **(1)** high value substrate – which connects chips to the board – for GPUs; **(2)** high-density-interconnects (HDI) in advanced types of accelerator modules; and **(3)** multi-layer PCBs for the GPU unit baseboard. Copper clad laminate (CCL), one of the main materials used in PCB, also has an opportunity to increase its content value as there's rising demand for CCL as this can enable higher speeds and higher frequencies.

Could AI just be all hype?

While we're bullish on AI, we are very aware of the hype surrounding past technologies like the metaverse, virtual reality and autonomous driving which have failed to deliver, so far, all that they promised. It's always been difficult to forecast precisely how far and fast emerging technology will emerge. For AI though, we do see real benefits for a wide range of users. The key question is how deeply generative AI applications can influence daily work and life, and whether there will be any killer applications. We are closely watching the development of multi-modal models, which can accept both text and images as inputs, and also Artificial General Intelligence (AGI) which is a system where computers have human-level cognitive abilities.

China's localisation opportunities and challenges

In our final chapter, we assess the current state of China's AI sector. On the software front, China is fairly competitive in terms of developing application-specific AI models, but there's a big gap between where China is in large language models (LLMs), or foundation models, and where the global leaders are. Still, there are plenty of reasons to be hopeful. In the first half of 2023, China tech companies released various LLMs and AI applications. In fact, 79 LLMs were launched in China by the end of May 2023 (source: Reuters, 30 May 2023).

On the hardware front, given there are US restrictions on China procuring certain GPUs and accelerators from overseas suppliers, we see ample localisation opportunities ahead in AI hardware. In particular, we believe China can improve in areas like chip architecture, processing nodes, interconnect bandwidth and memory capacity.

Foundation models are accelerating computational requirements

Finally, a quick word on the big picture and how fast the industry is changing. According to Nvidia, computational requirements are now set to increase eight times every two years for models excluding transformers (deep learning architecture), but that ratio surges to a staggering 275x for transformer-based AI models. That means today's computation power will double, on average, every three-and-a-half months. However, the problem is Moore's Law states that semiconductor transistors on a chip double only every two years. This means the industry needs to develop more advanced computing devices to catch up with rapidly evolving AI models.

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The full note takes a deep dive into each of the eight subsectors mentioned above, along with a look at China's localisation opportunities and challenges.

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